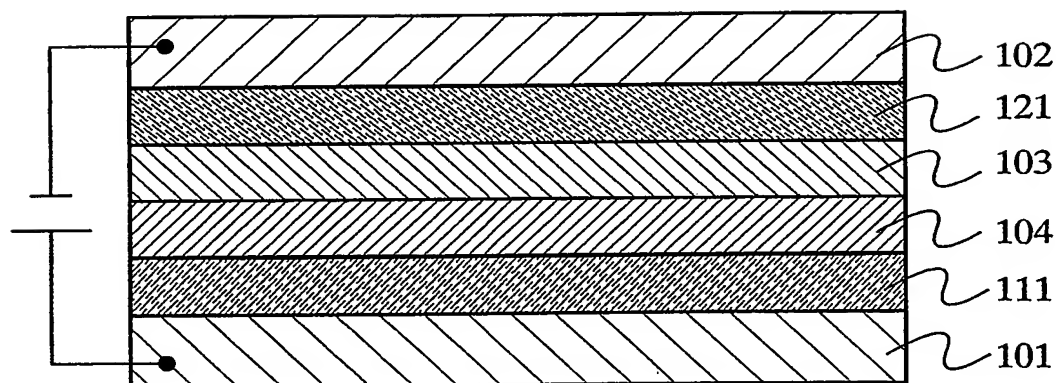


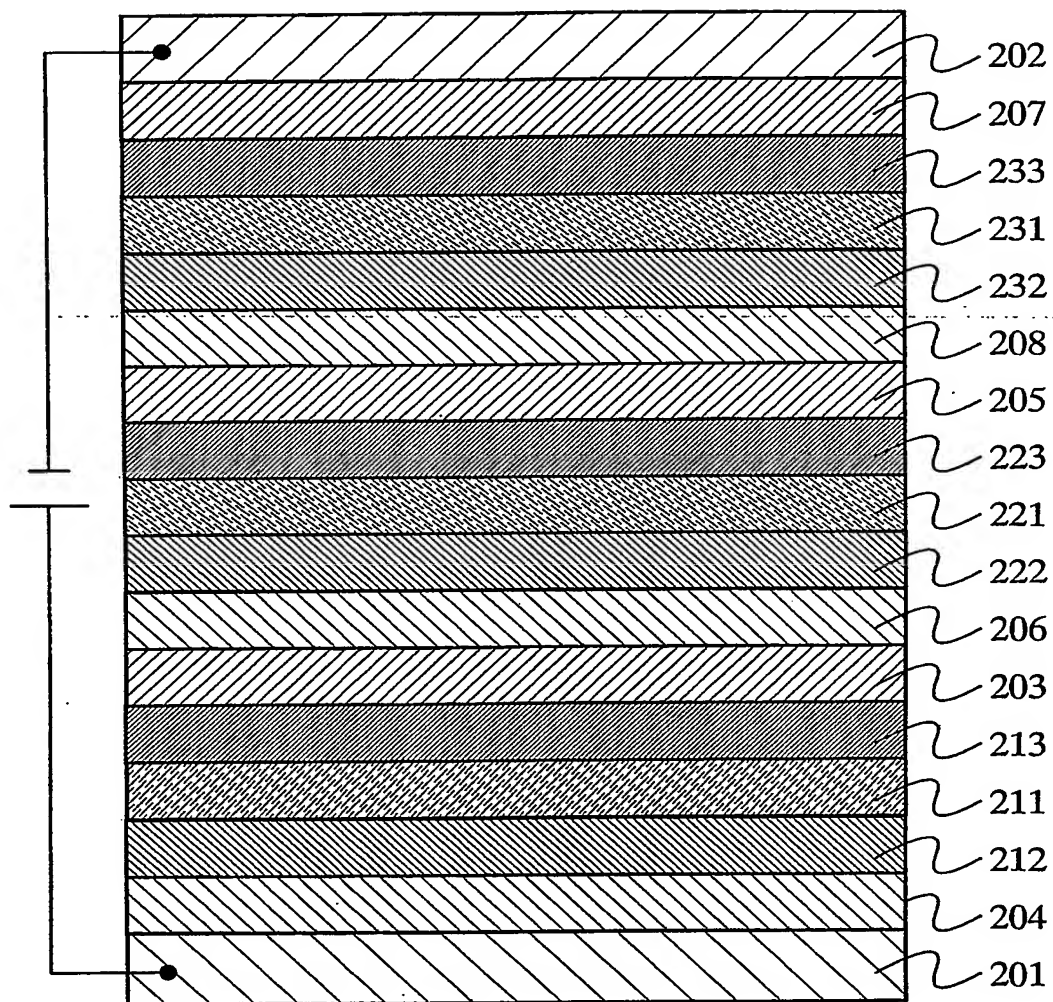
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FIG. 1



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FIG. 2



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FIG. 3

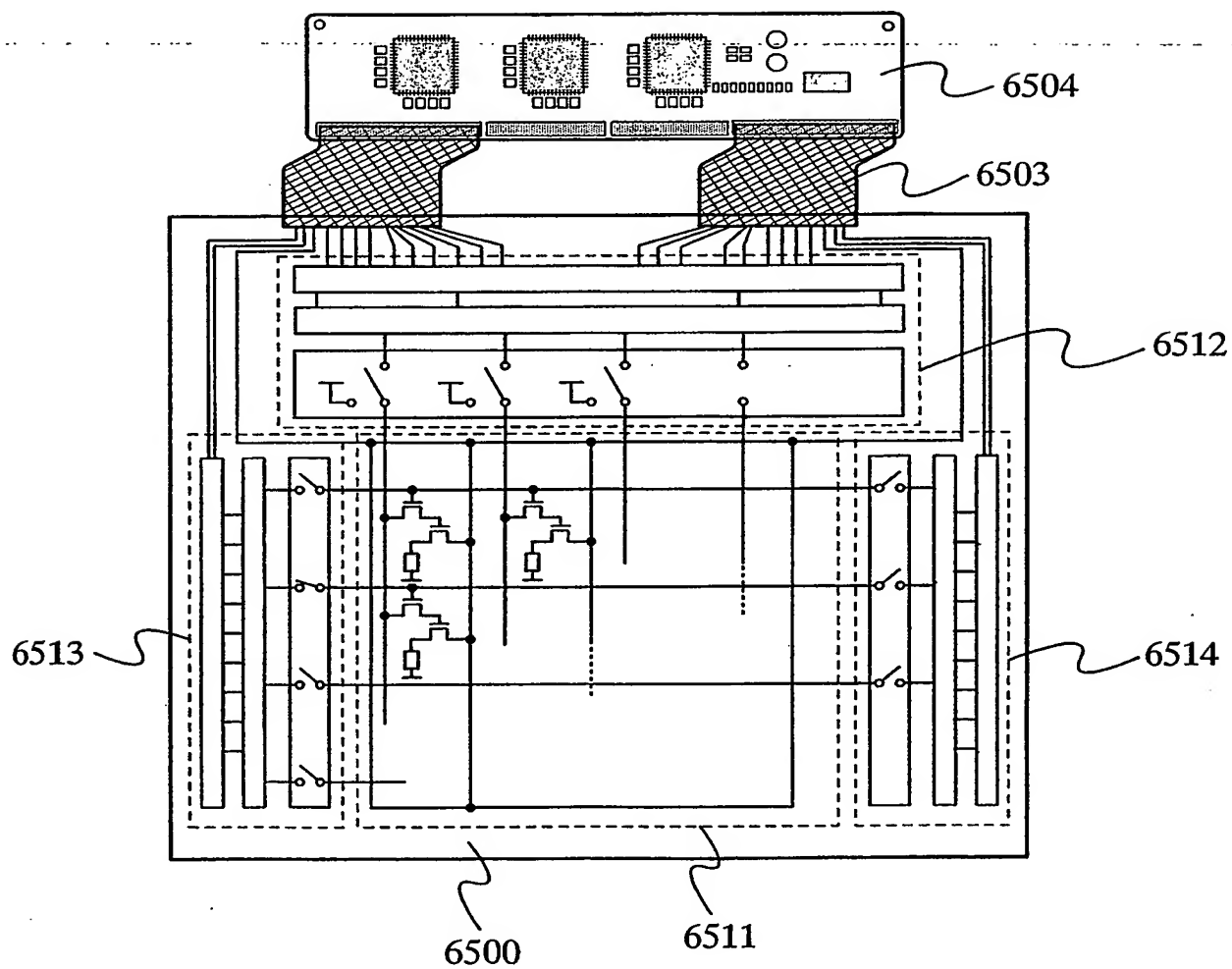
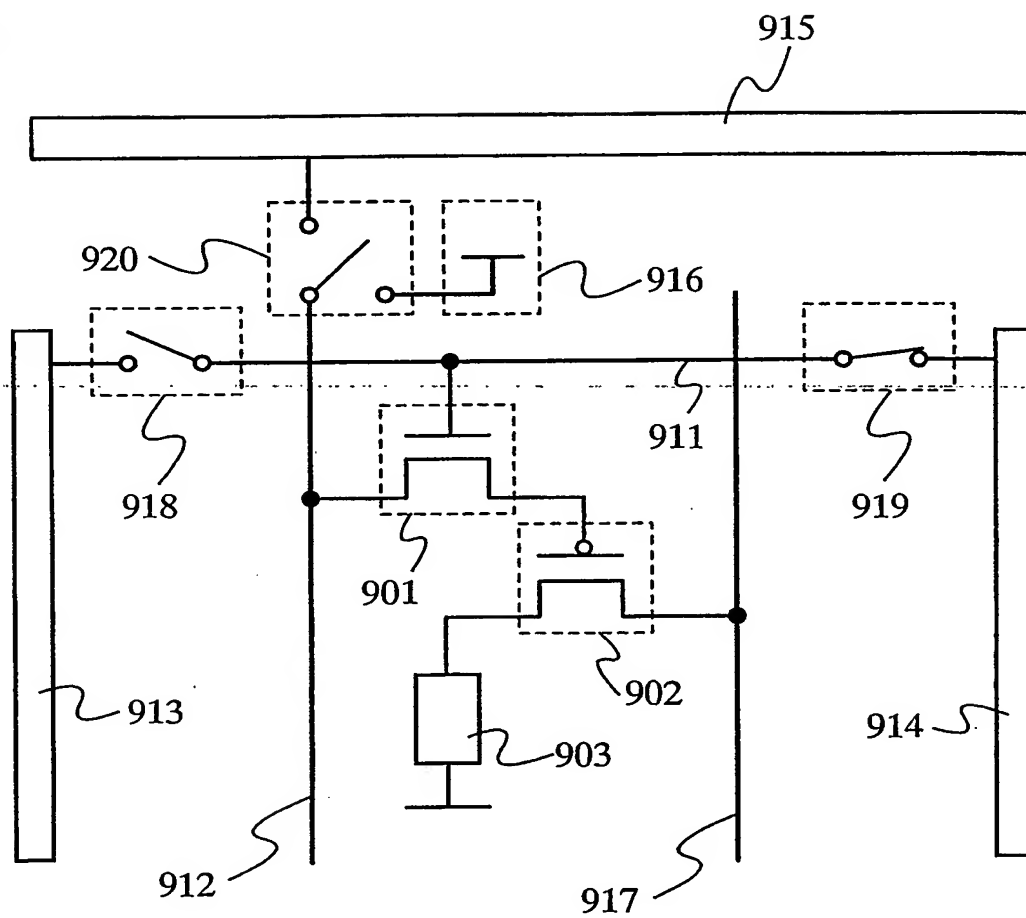


FIG. 4



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FIG. 5

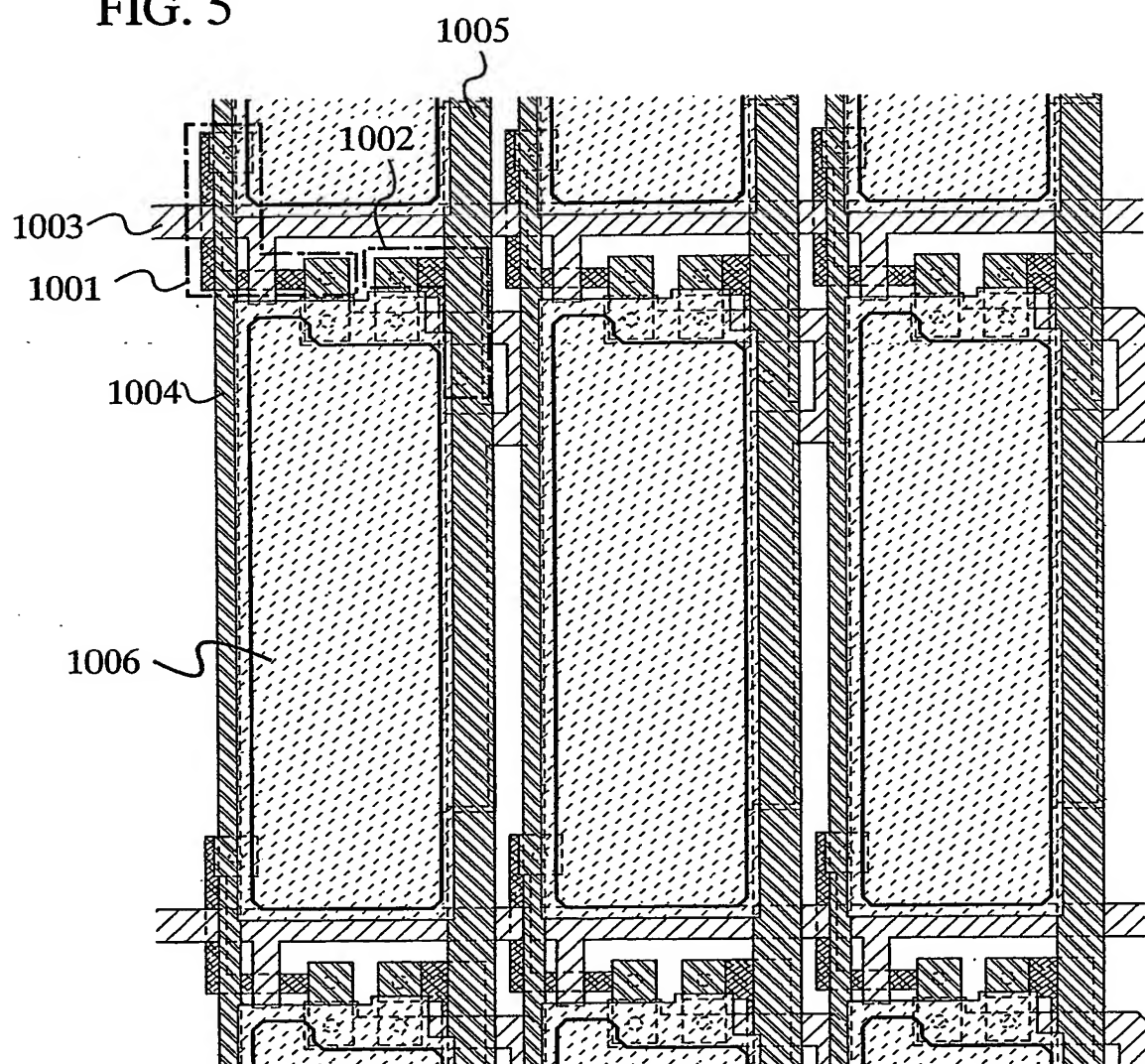
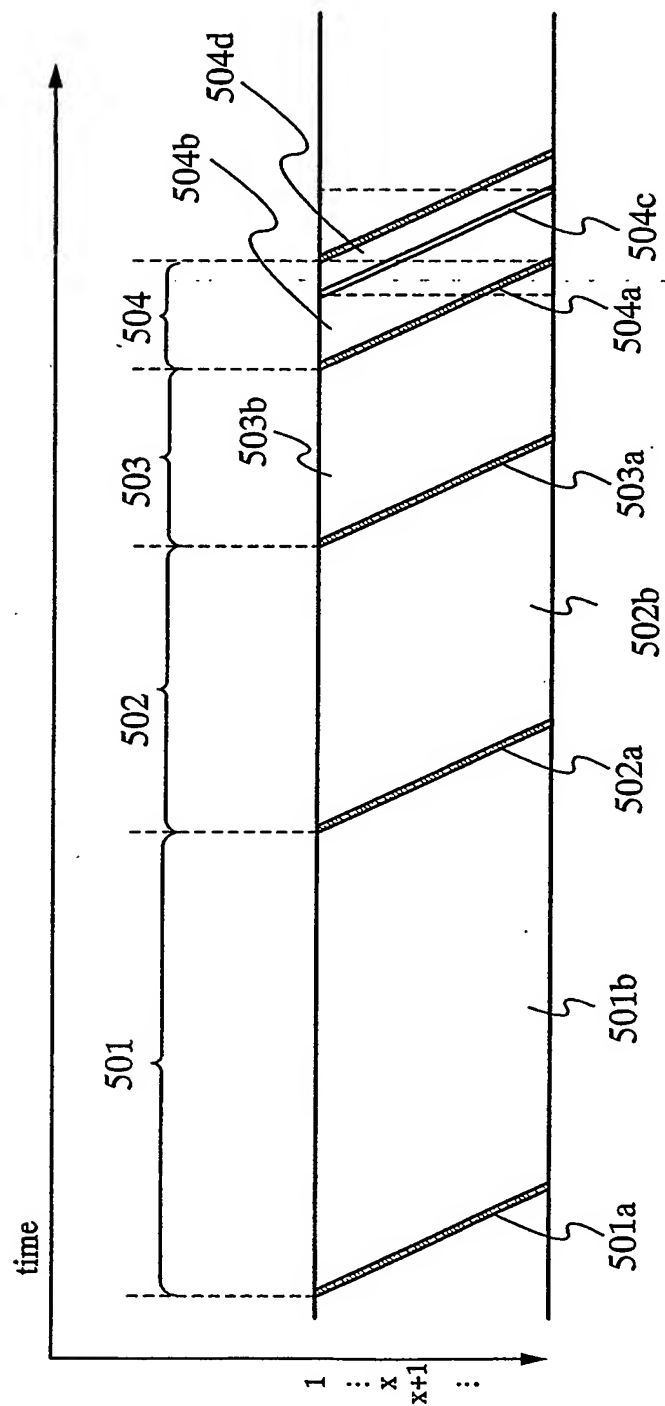


FIG. 6



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FIG. 7A

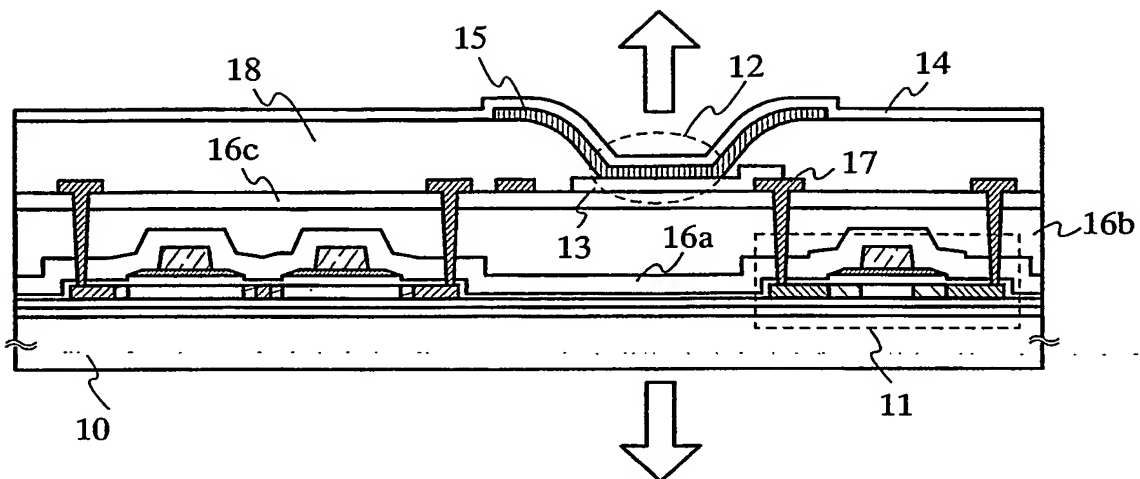


FIG. 7B

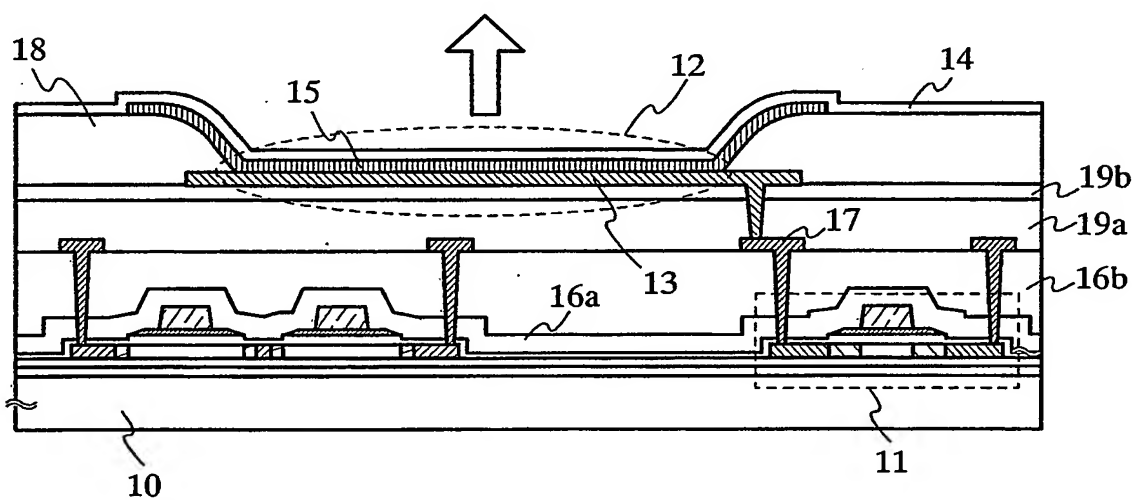
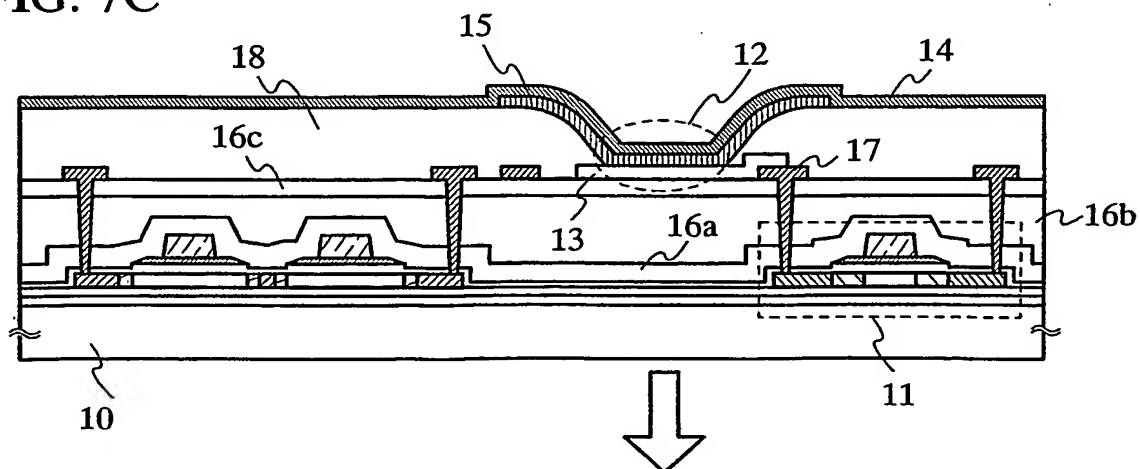


FIG. 7C



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FIG. 8A

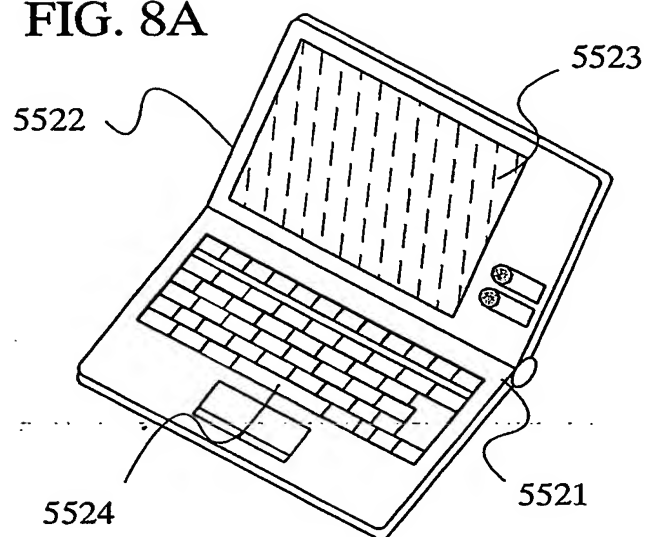


FIG. 8B

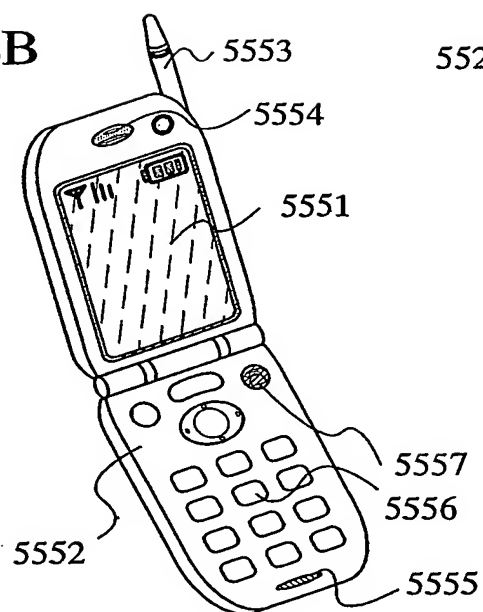
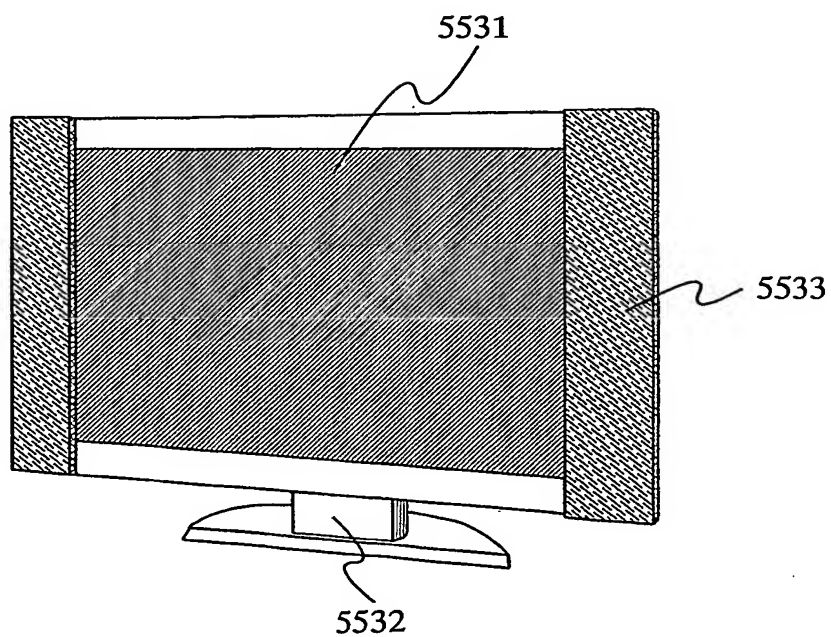


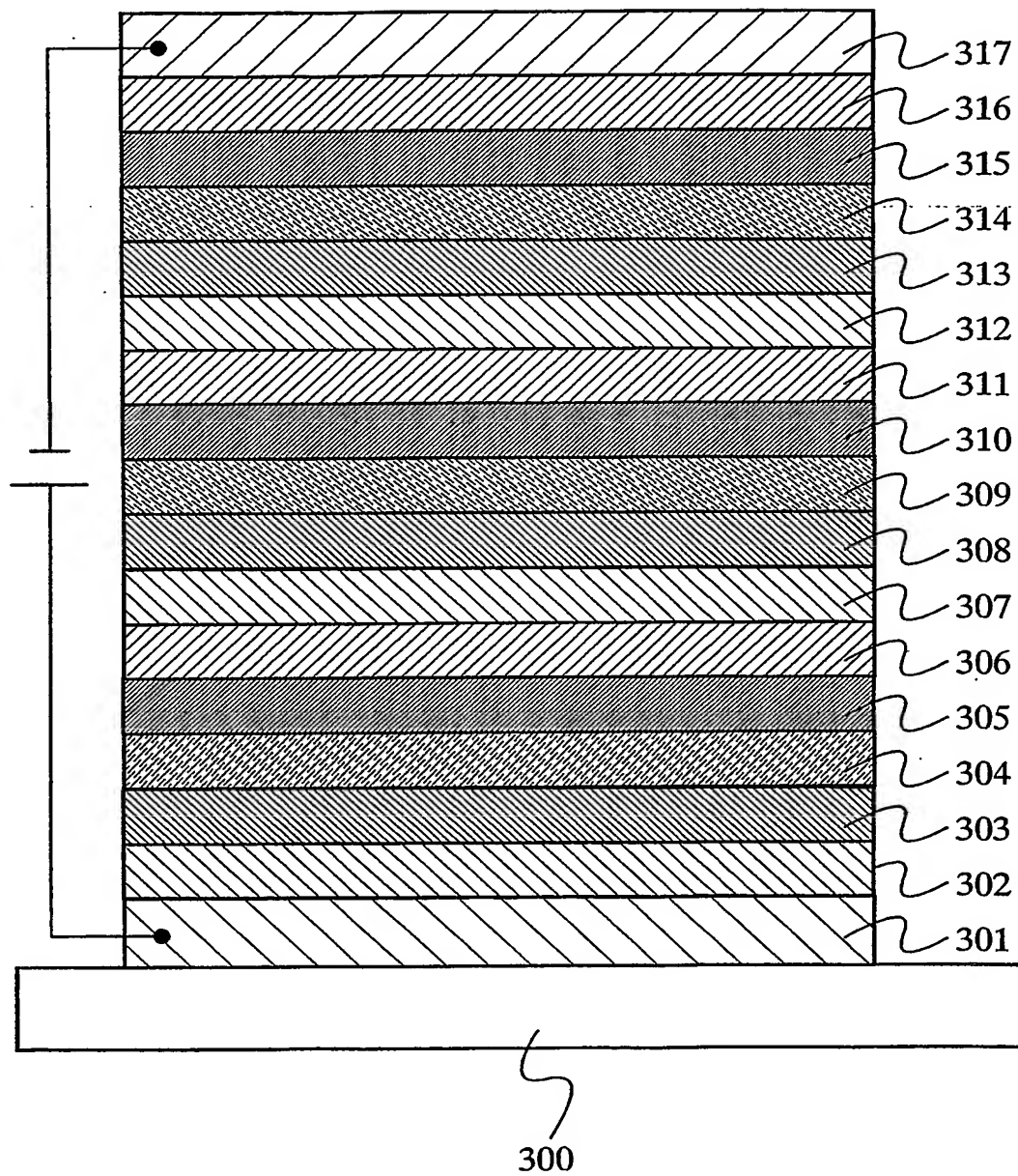
FIG. 8C





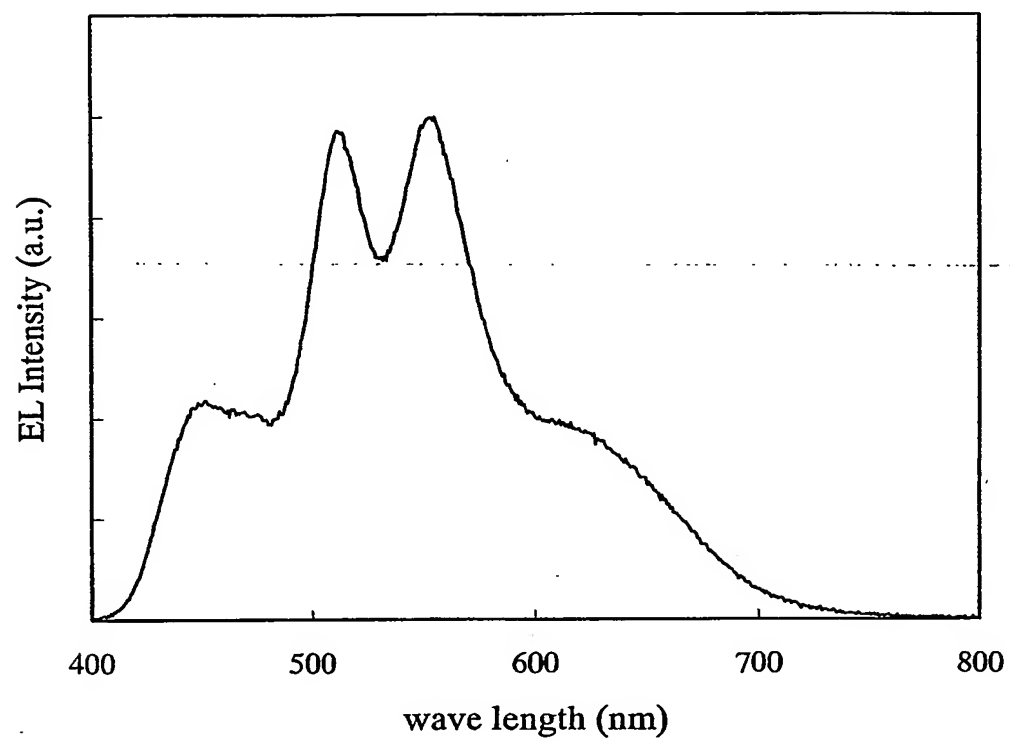
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FIG. 9



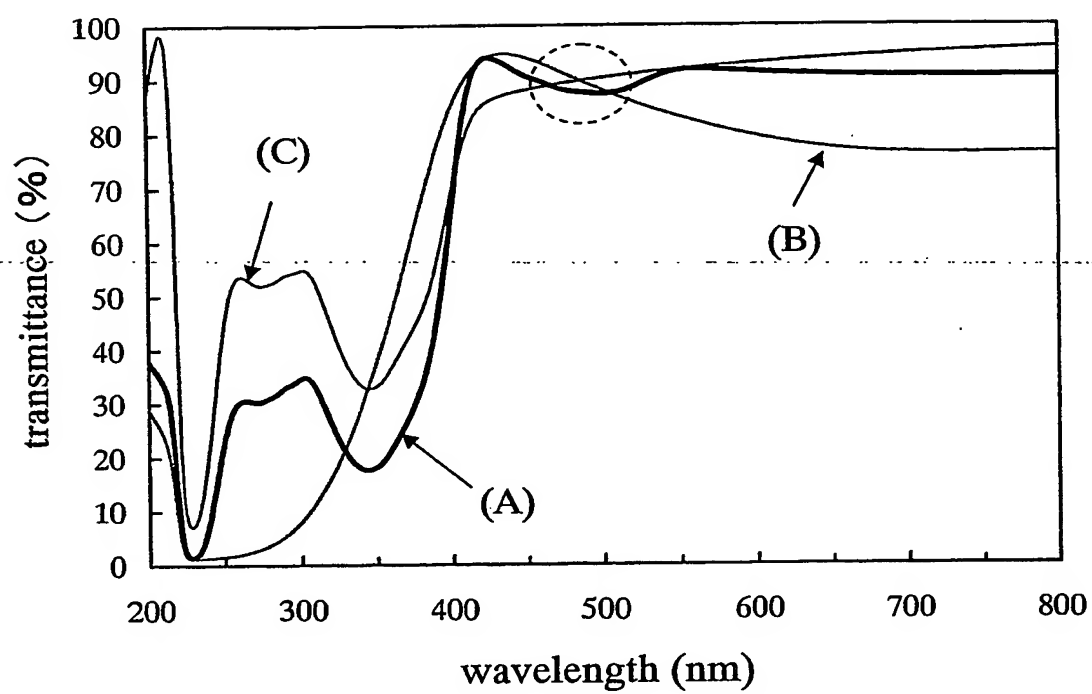
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FIG. 10



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FIG. 11



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EXPLANATION OF REFERENCE

10: substrate, 11: transistor, 12: light emitting element, 13: first electrode, 14: second electrode, 15: layer, 16: first interlayer insulating film, 16a: first interlayer insulating film, 16b: first interlayer insulating film, 16c: first interlayer insulating film, 17: wiring, 18: partition wall layer, 19: second interlayer insulating film, 101: first electrode, 102: second electrode, 103: first layer, 104: second layer, 111: first light emitting layer, 121: second light emitting layer, 201: first electrode, 202: second electrode, 203: first layer, 204: second layer, 205: first layer, 206: second layer, 207: first layer, 208: second layer, 211: first light emitting layer, 212: hole transporting layer, 213: electron transporting layer, 221: second light emitting layer, 222: hole transporting layer, 223: electron transporting layer, 231: third light emitting layer, 232: hole transporting layer, 233: electron transporting layer, 300: substrate, 301: layer, 302: layer, 303: layer, 304: layer, 305: layer, 306: layer, 307: layer, 308: layer, 309: layer, 310: layer, 311: layer, 312: layer, 313: layer, 314: layer, 315: layer, 316: layer, 317: layer, 501: sub-frame, 501a: writing period, 501b: holding period, 502: sub-frame, 502a: writing period, 502b: holding period, 503: sub-frame, 503a : writing period, 503b: holding period, 504: sub-frame, 504a: writing period, 504b: holding period, 504c: erasing period, 504d: non-light emitting period, 901: first transistor, 902: second transistor, 903: light emitting element, 911: gate signal line, 912: source signal line, 913: writing gate signal line driver circuit, 914: erasing gate signal line driver circuit, 915: source signal line driver circuit, 916: power source, 917: current supply line, 918: switch, 919: switch, 920: switch, 1001: first transistor, 1002: second transistor, 1003: gate signal line, 1004: source signal line, 1005: current supply line, 1006: electrode, 5521: main body, 5522: housing, 5523: display portion, 5524: keyboard, 5531: display portion, 5532: housing, 5533: speaker, 5551: display portion, 5552: main body, 5553: antenna, 5554: audio output portion, 5555: audio input portion, 5556: operation switch, 6500: substrate, 6503: FPC, 6504: printed wiring board (PWB), 6511: pixel portion, 6512: source signal line driver circuit, 6513: writing gate signal line driver circuit, 6514: erasing gate signal line driver circuit

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